

9-26-00
M.L.**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: : Examiner:
 Jean-Francois AUTECHAUD :
 Serial No.: To Be Assigned : Group Art Unit:
 Filed: April 26, 2000 : Corres. To FR99/05266
 For: MODULAR INTERCONNECTION : Filed April 26, 1999
 ARCHITECTURE FOR AN EXPANDABLE :
 MULTIPROCESSOR MACHINE, USING A :
 MULTILEVEL BUS HIERARCHY AND THE :
 SAME BUILDING BLOCK FOR ALL THE :
 LEVELS :

McLean, Virginia

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents and Trademarks
 Washington, DC 20231

Sir:

Prior to examination of the above-identified application, please amend the application as follows:

IN THE SPECIFICATION:

Page 1, after the title and before the first paragraph, insert the following headings:

--BACKGROUND OF THE INVENTION

A1

Field of the Invention--;

Page 1, line 12, before the paragraph beginning "In such machines...", insert the following heading at the left hand margin:

--Description of Related Art--

Page 1, line 9, before the paragraph beginning "The object of the invention...", insert the following heading at the left hand margin: